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Features

- ‡ New technology for high voltage device.
- ‡ Low on-resistance and low conduction losses
- ‡ 80WUD /RZ *DWH &KDUJH FDXVH ORZHU
- ‡ 100% Avalanche Tested

| | | |
|---------------|-----|---|
| BVDSS | 650 | V |
| ID | 78 | A |
| RDSON@VGS=10V | 33 | m |

Applications

- ‡ Power factor correction PFC
- ‡ Switched mode power supplies(SMPS)
- ‡ Uninterruptible Power Supply UPS



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| | | | |
|---------|----------|---------|--------|
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| F 78!2E | % + < EC | F 78!2E | = 2 |

\$ + # -)) . # / 0 1 2

| | | | |
|------------------|----------|-------------|-----|
|) | 3)+ | : | 4 |
| " + # 6 | 6"# | 7 2 | 6 |
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| " / 2 > < 8 A | B \$ " & | C : | * |
| ' - - " \$ > < 8 | B \$ & | < = E | * |
| " (/ 12 | B | C : | * |
| # | *# | < = 7 2 | - F |
| % F # - ! | F # 1 | + 8 8 H 8 2 | A |
| - ! F + + | ! F | 2 < 8 | A . |



!

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| ! 6 ((# # | | | | | | | |
| " + # J / 6 | J 6 ## | | 6 _{1#} > 2 B 8 2 * | 7 8 2 | | | 6 |
| L 1 6 " | B ## | | 6 _# > 7 8 2 6 _# > 2 6 | | | 2 | K * |
| 1 + J / | B ## | | 6 _{1#} > M 2 6 _# 2 | | | M 2 2 | * |
| 1 6 | 6 _{1#} \$ & | | 6 _# > 6 _# B < 8 2 * | = | E | 8 | 6 |
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| B | | | 6 _# > 2 2 6 6 _# > 2 | | 7 2 C 2 | | (|
| % | | | (> ' 4 @ | | < < 2 | | (|
| ! | | | | | = 8 | | (|
| 1 | N | | 6 _# > E : 2 6 _# B = 2 * | | 2 = | | |
| 1 + # | N | | 6 _{1#} > 2 6 | | < 7 < | | |
| 1 + " | N | | | | = 2 | | |
|) # | | | | | | | |
| + " - | \$ & | | V _{DS} = 300 V, I _D = 30 A, V _{GS} = 10 V, R _{GEN} = 25 Ω | | 64 | | # |
| + ! - | | | | 69 | | # | |
| + % " - | \$ & | | | 307 | | # | |
| + % (- | | | | 56 | | # | |
| & # # | | | | | | | |
| (% 6 | 6 _# | | > 8 A # B E 2 * 1 # 6 > 2 | | | < | 6 |
| ! ! - | | | VR=400V I _F = 20 A, dI/dT = 100 A/μS | | 165 | | # |
| ! ! / 5 2 | N | | | | 1.35 | | |
| / ! ! | B. | | | | 15 | | * |

! ! O - 5 - - -
 < 6 " " > 8 2 6 6 1 # > 2 6 ! 1 > < 8 ;
 = ≤ = 2 2 ≤ < 3

Typical Characteristics

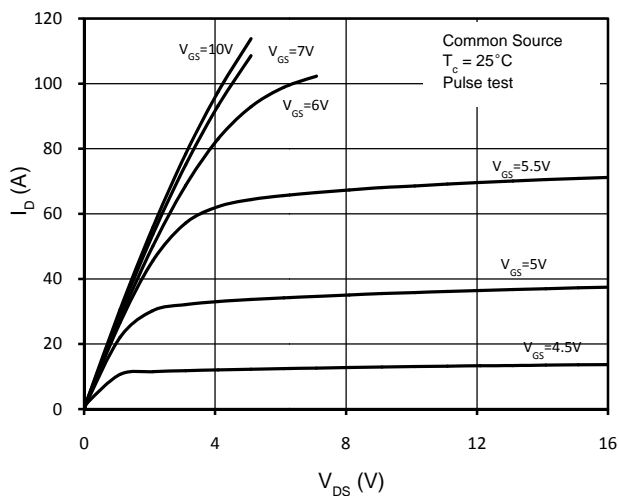


Figure 1. On-Region Characteristics

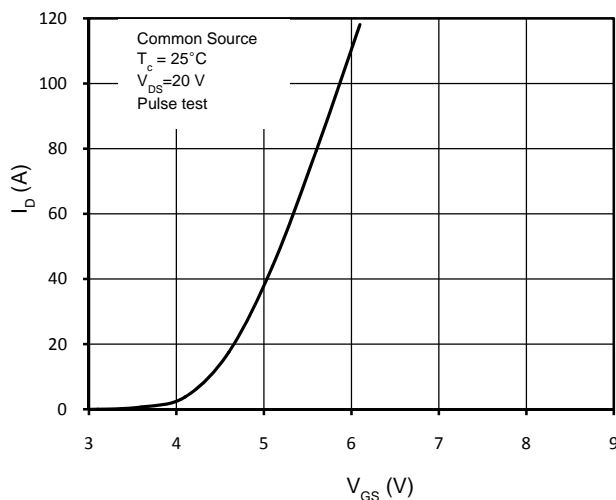


Figure 2. Transfer Characteristics

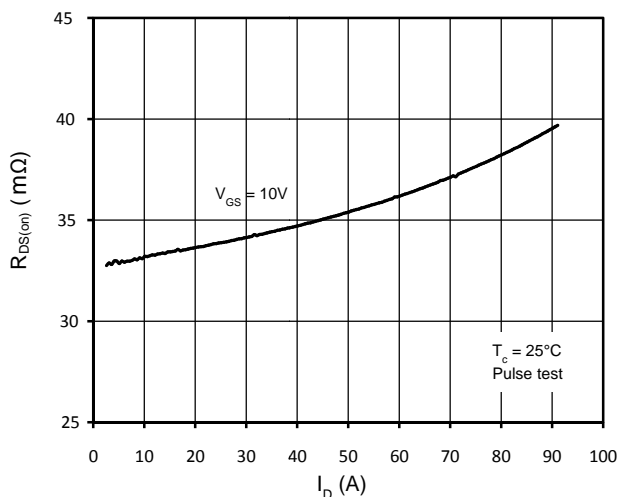


Figure 3. Static Drain-Source On Resistance

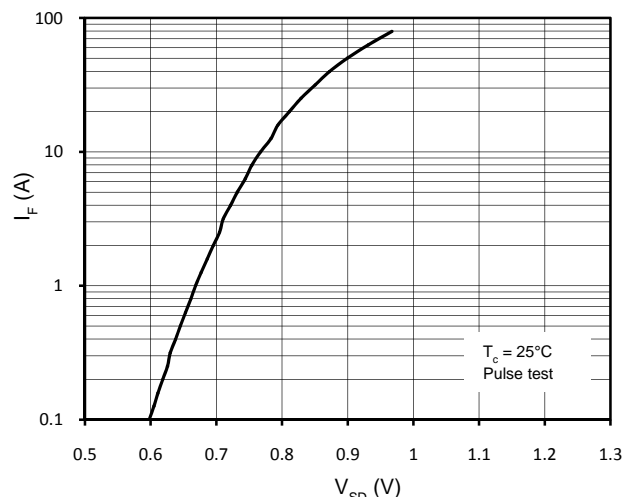


Figure 4. Body-Diode Forward Characteristics

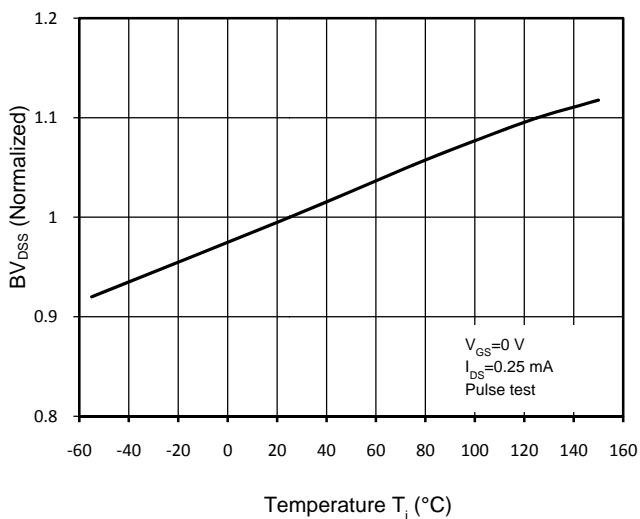


Figure 5. Normalized BV_{DS} vs. Temperature

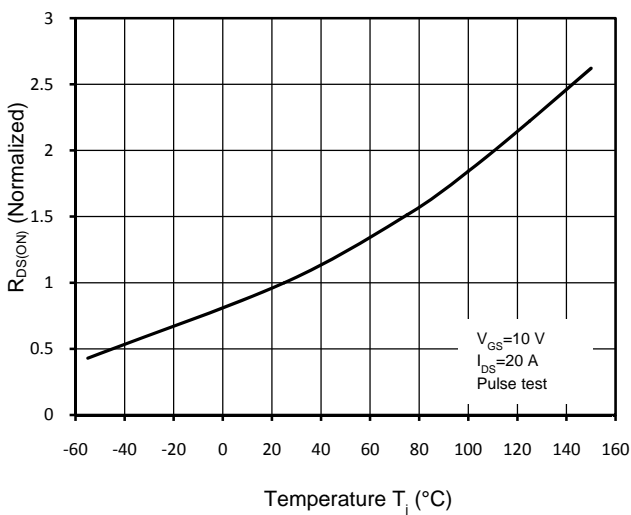


Figure 6. Normalized $R_{DS(on)}$ vs. Temperature

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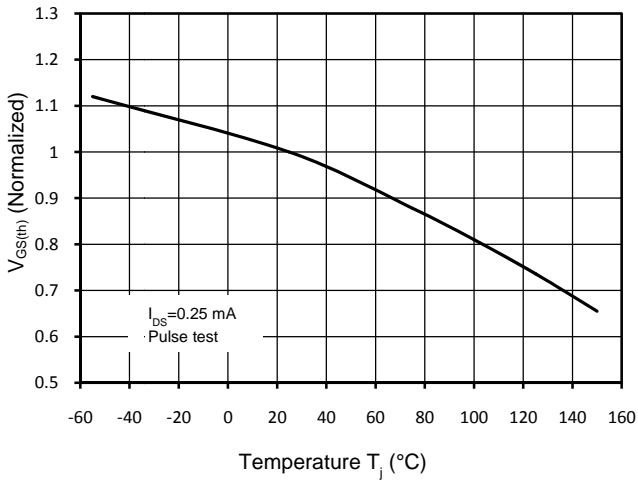


Figure 7. Threshold Voltage vs. Temperature

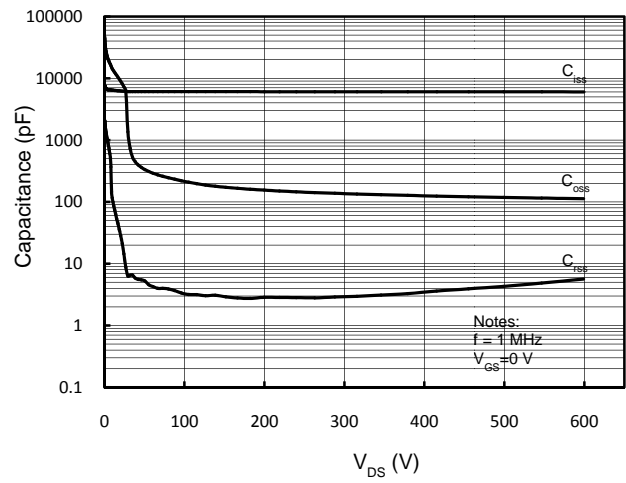


Figure 8. Capacitance Characteristics

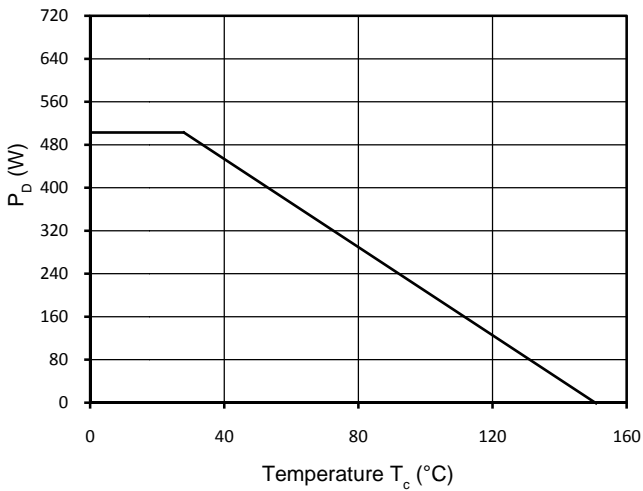


Figure 9. Power Dissipation

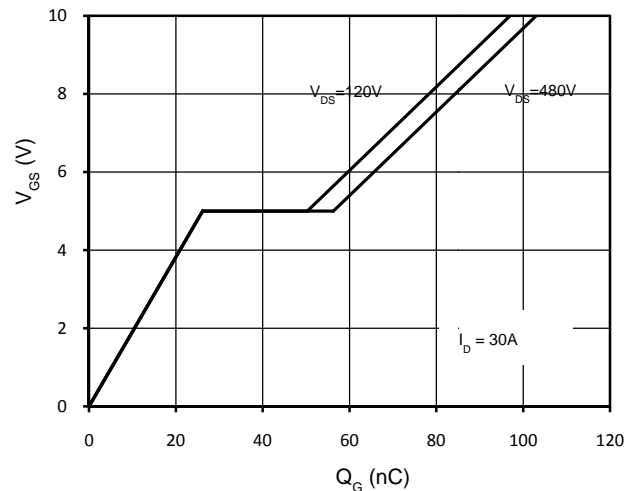


Figure 10. Gate Charge Characteristics

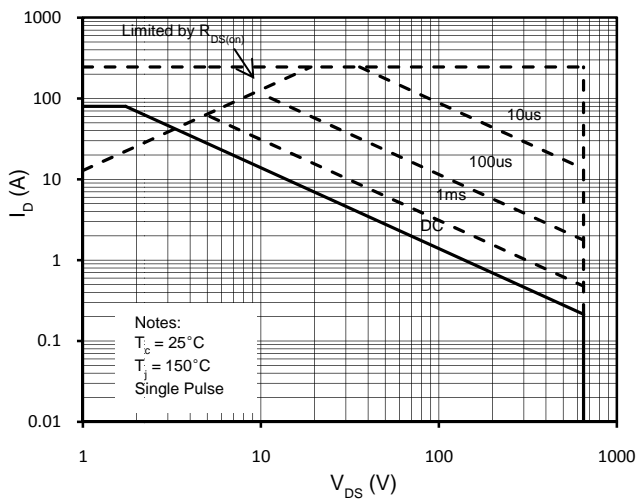


Figure 11. Maximum Safe Operating Area

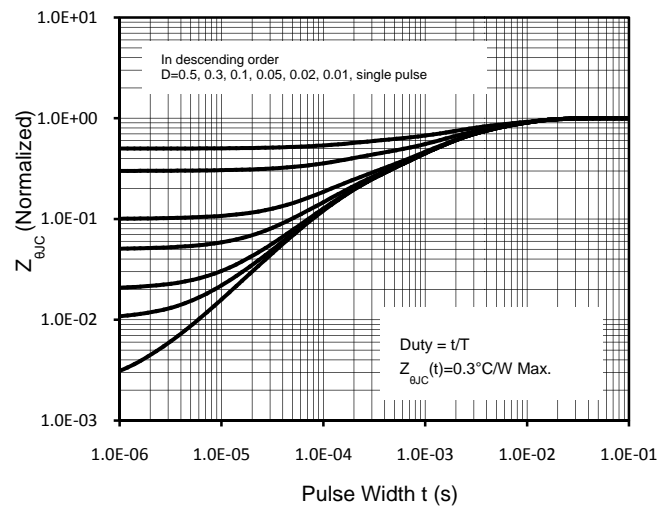
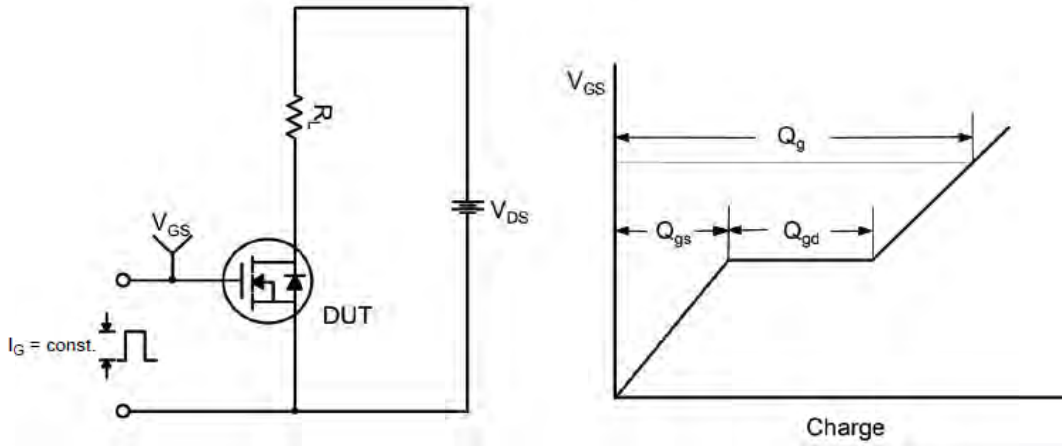


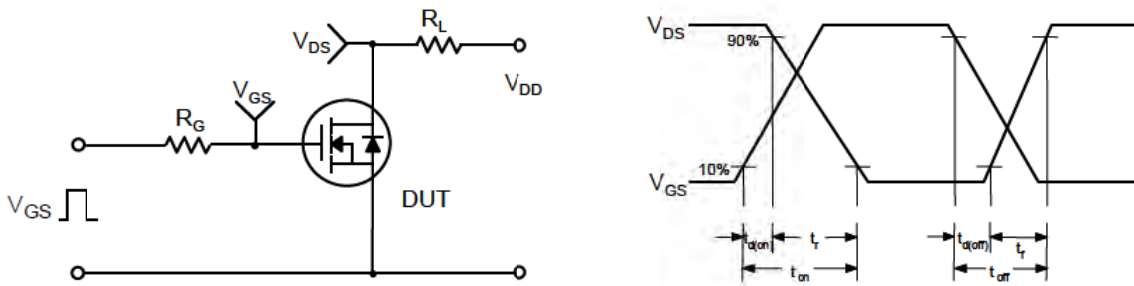
Figure 12. Transient Thermal Response Curve

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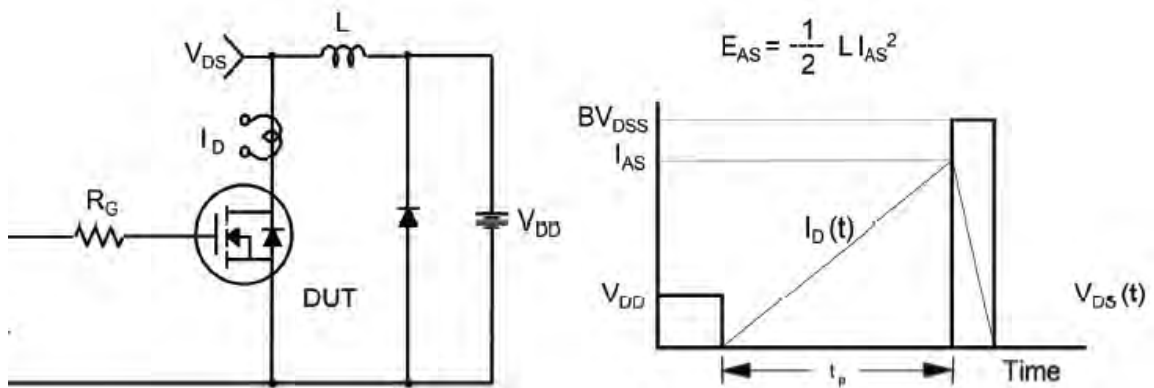
Gate Charge Test Circuit & Waveform



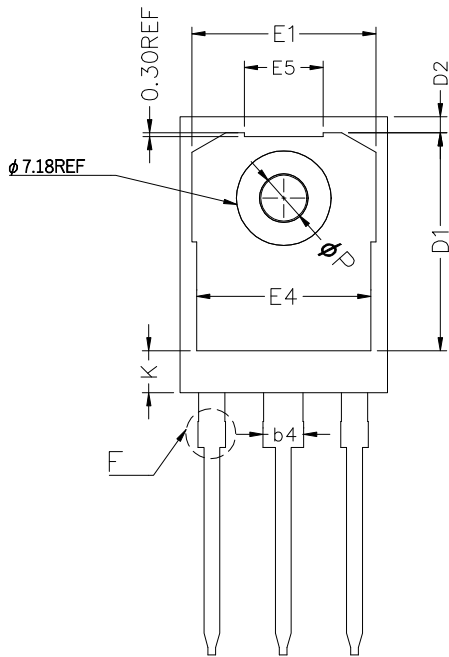
Switching Test Circuit & Waveforms



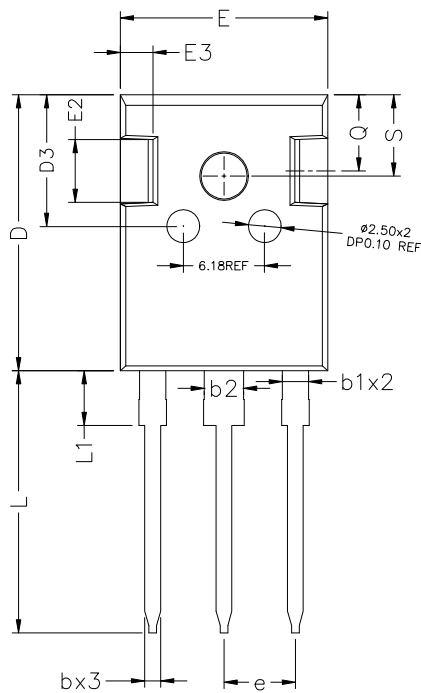
Unclamped Inductive Switching Test Circuit & Waveforms



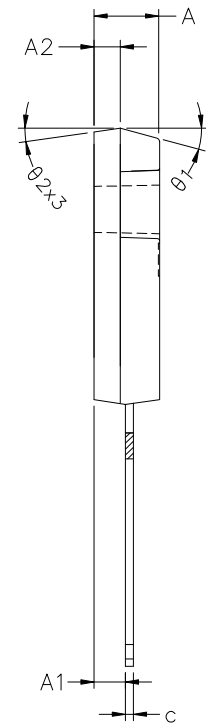
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TO-247 Package Outline Dimensions (Units: mm)


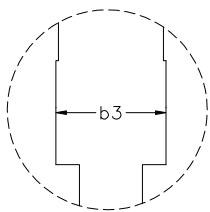
BOTTOM VIEW



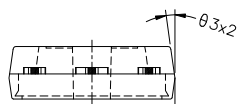
TOP VIEW



SIDE VIEW



DETAIL F



SIDE VIEW

**COMMON DIMENSIONS
(UNITS OF MEASURE IS mm)**

| | MIN | NORMAL | MAX |
|---------|-----------|--------|--------|
| A | 4.900 | 5.000 | 5.100 |
| A1 | 2.300 | 2.400 | 2.500 |
| A2 | 1.900 | 2.000 | 2.100 |
| b | 1.070 | 1.200 | 1.330 |
| b1 | 1.910 | 2.000 | 2.160 |
| b2 | 2.870 | 3.000 | 3.160 |
| b3 | 1.910 | 2.100 | 2.410 |
| b4 | 2.870 | 3.000 | 3.380 |
| c | 0.550 | 0.600 | 0.680 |
| D | 20.000 | 21.000 | 21.100 |
| D1 | 16.250 | 16.950 | 17.650 |
| D2 | 1.200REF | | |
| D3 | 10.000REF | | |
| E | 15.700 | 15.800 | 15.900 |
| E1 | 13.100 | 14.020 | 14.150 |
| E2 | 3.680 | — | 5.100 |
| E3 | 1.000 | — | 1.900 |
| E4 | 12.380 | 13.260 | 13.430 |
| E5 | 5.990REF | | |
| e | 5.440BSC | | |
| L | 19.810 | 19.950 | 20.320 |
| L1 | 4.100 | — | 4.400 |
| phi P | 3.500 | — | 3.650 |
| Q | 5.490 | — | 6.000 |
| S | 6.040 | — | 6.300 |
| theta 1 | 15.0°REF | | |
| theta 2 | 8.0°REF | | |
| theta 3 | 8.0°REF | | |
| K | 3.190REF | | |