

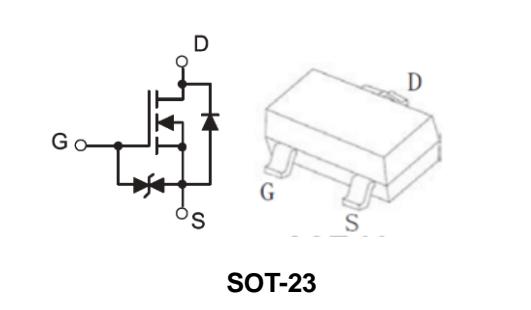
**50V/0.34A N-Channel Advanced Power MOSFET**
**Features**

- Trench Power MV MOSFET technology
- Voltage controlled small signal switch
- Low input Capacitance
- Fast Switching Speed
- Low Input / Output Leakage

BVDSS	50	V
ID	0.34	A
RDSON@VGS=10V	1.29	Ω
RDSON@VGS=4.5V	1.5	Ω

**Applications**

- Battery operated systems
- Solid-state relays
- Direct logic-level interface: TTL/CMOS


**Order Information**

Product	Package	Marking	Reel Size	Reel	Carton
BSS138	SOT-23	J1	7inch	3000PCS	180000PCS

**Absolute Maximum Ratings**

Symbol	Parameter	Rating	Unit	
<b>Common Ratings (TC=25°C Unless Otherwise Noted)</b>				
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	50	V	
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V	
$T_J$	Maximum Junction Temperature	150	°C	
$T_{STG}$	Storage Temperature Range	-55 to 150	°C	
$I_S$	Diode Continuous Forward Current	TA =25°C	0.3	A
<b>Mounted on Large Heat Sink</b>				
$I_{DM}$	Pulse Drain Current Tested (Silicon Limit) (Note1)	TA =25°C	1.2	A
$I_D$	Continuous Drain current	TA =25°C	0.34	A
$P_D$	Maximum Power Dissipation	TA =25°C	0.35	W
$R_{θJA}$	Thermal Resistance Junction-to-Ambient (Note2)		357	°C/W

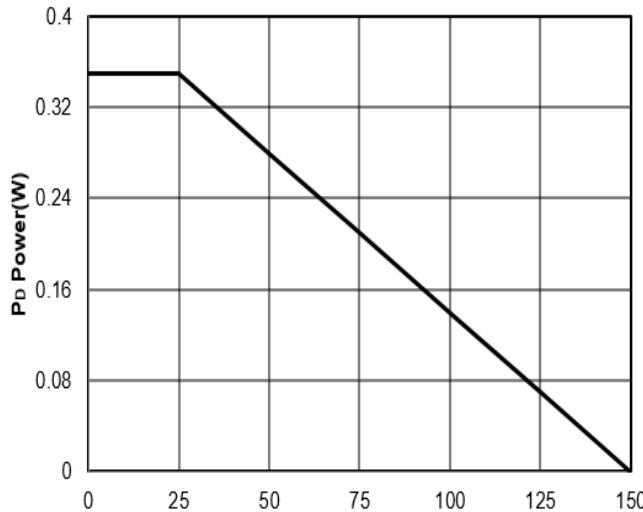
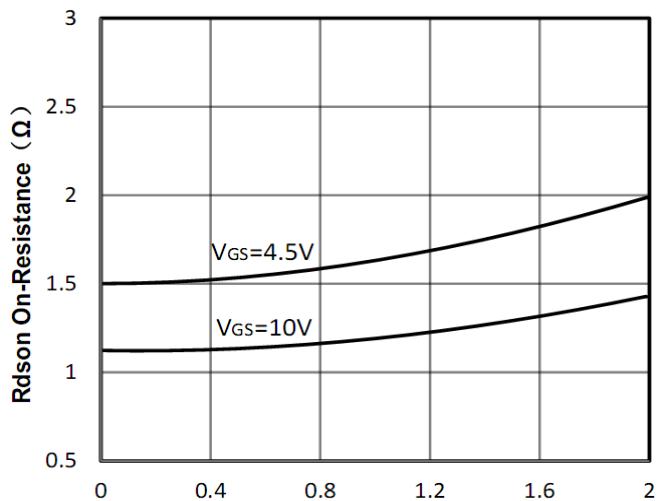
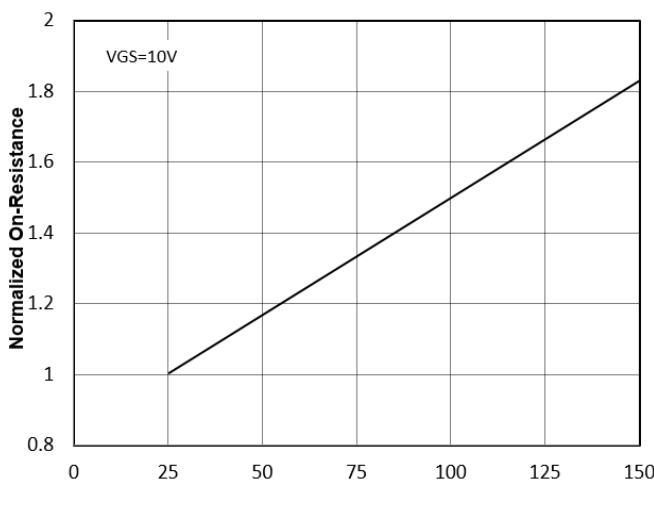
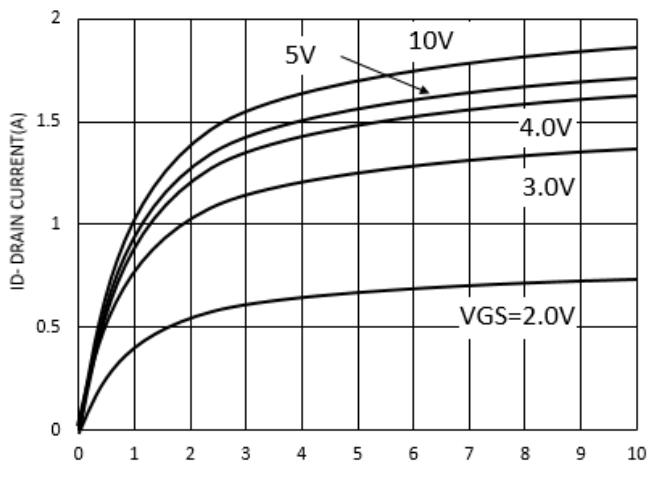
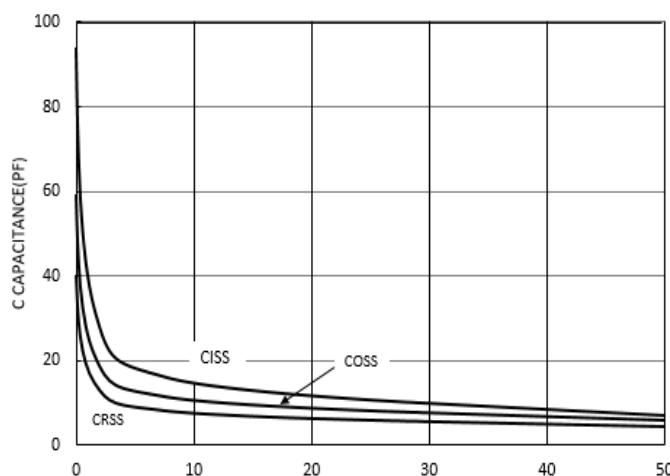
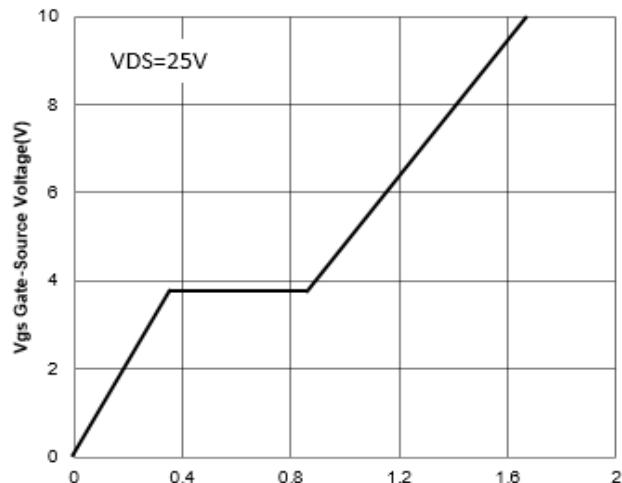


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Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ TJ = 25°C (unless otherwise stated)</b>						
$V_{(BR)DSS}$	Drain- Source Breakdown Voltage	$VGS=0V$ $ID=250\mu A$	50	--	--	V
$I_{DSS}$	Zero Gate Voltage Drain current	$VDS=50V$ , $VGS=0V$	--	--	1	$\mu A$
$I_{GSS}$	Gate-Body Leakage Current	$VGS=\pm 20V$ , $VDS=0V$	--	--	$\pm 10$	$\mu A$
$V_{GS(TH)}$	Gate Threshold Voltage	$VDS=VGS$ , $ID=250\mu A$	0.8	1.2	1.6	V
$R_{DS(ON)}$	Drain-Source On-State Resistance (Note3)	$VGS=10V$ , $ID=0.3A$	--	1.29	2.5	$\Omega$
		$VGS=4.5V$ , $ID=0.2A$	--	1.5	3	$\Omega$
<b>Dynamic Electrical Characteristics @ TJ = 25°C (unless otherwise stated) (Note4)</b>						
$C_{iss}$	Input Capacitance	$VDS=25V$ , $VGS=0V$ , $F=1MHz$	--	17.5	--	pF
$C_{oss}$	Output Capacitance		--	11.5	--	pF
$C_{rss}$	Reverse Transfer Capacitance		--	6.5	--	pF
$Q_g$	Total Gate Charge	$VGS=10V$ , $VDS=25V$ , $F=1MHz$	--	1.7	2.4	nC
<b>Switching Characteristics (Note4)</b>						
$t_{d(on)}$	Turn-on Delay Time	$VDS=25V$ , $ID=0.3A$ , $RG=6\Omega$ , $VGS=10V$	--	5	--	nS
$t_r$	Turn-on Rise Time		--	18	--	nS
$t_{d(off)}$	Turn-off Delay Time		--	36	--	nS
$t_f$	Turn-off Fall Time		--	14	--	nS
<b>Source- Drain Diode Characteristics@ TJ = 25°C (unless otherwise stated)</b>						
$V_{SD}$	Forward on voltage	$IS=0.3A$ , $VGS=0V$	--	--	1.2	V

Note:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: pulse width  $\leq 300$  us, duty cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production testing.

**50V/0.34A N-Channel Advanced Power MOSFET**
**Typical Characteristics**

**Figure1: TJ Junction Temperature (°C)**

**Figure2: Id Drain Current (A)**

**Figure3: TJ Junction Temperature (°C)**

**Figure4: VDS Drain-Source Voltage (V)**

**Figure5: VDS Drain-Source Voltage (V)**

**Figure6: Qg Gate Charge (nC)**

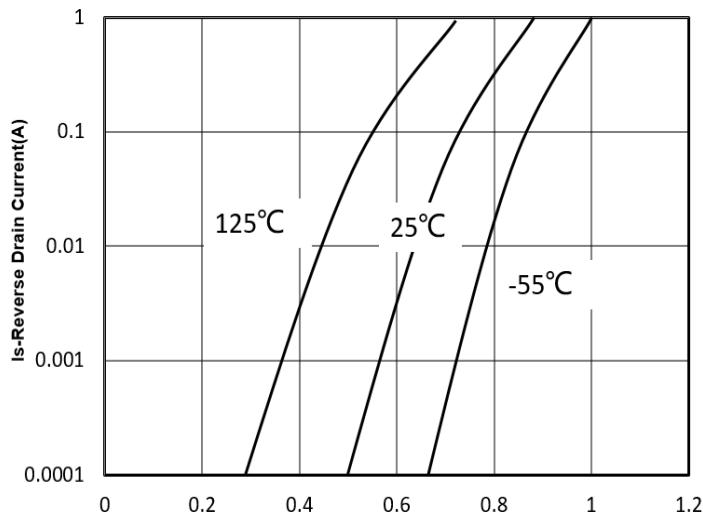
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Figure 7: V<sub>sd</sub> Source-Drain Voltage (V)

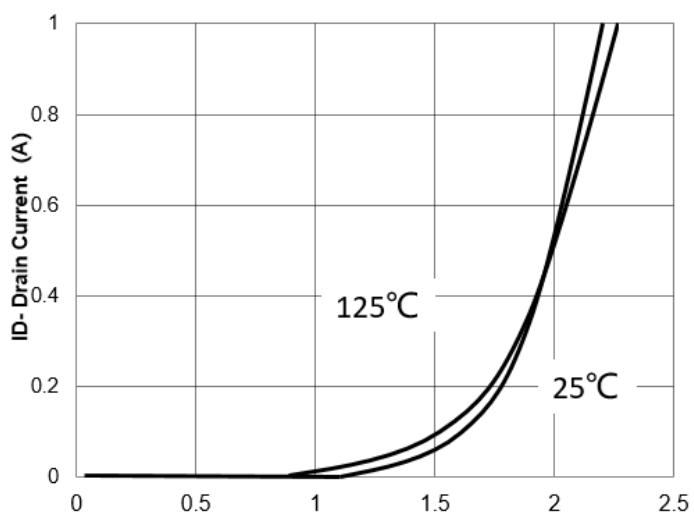


Figure 8: V<sub>gs</sub> Gate-Source Voltage (V)

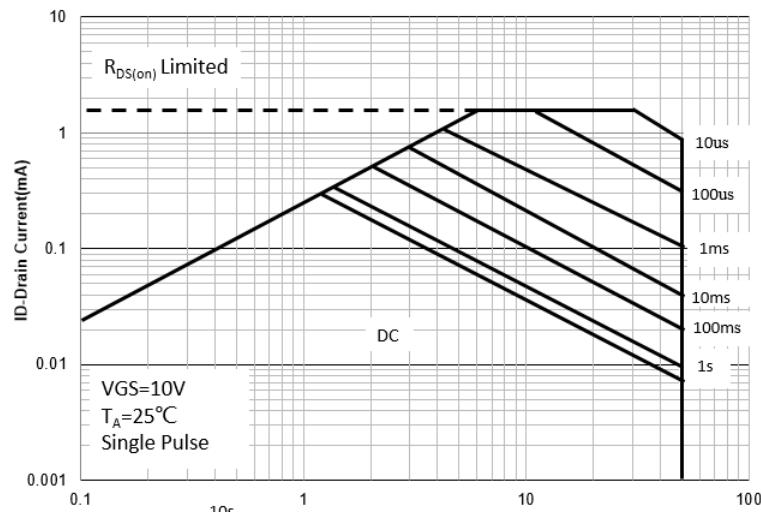


Figure 9: V<sub>sd</sub> Drain -Source Voltage (V)

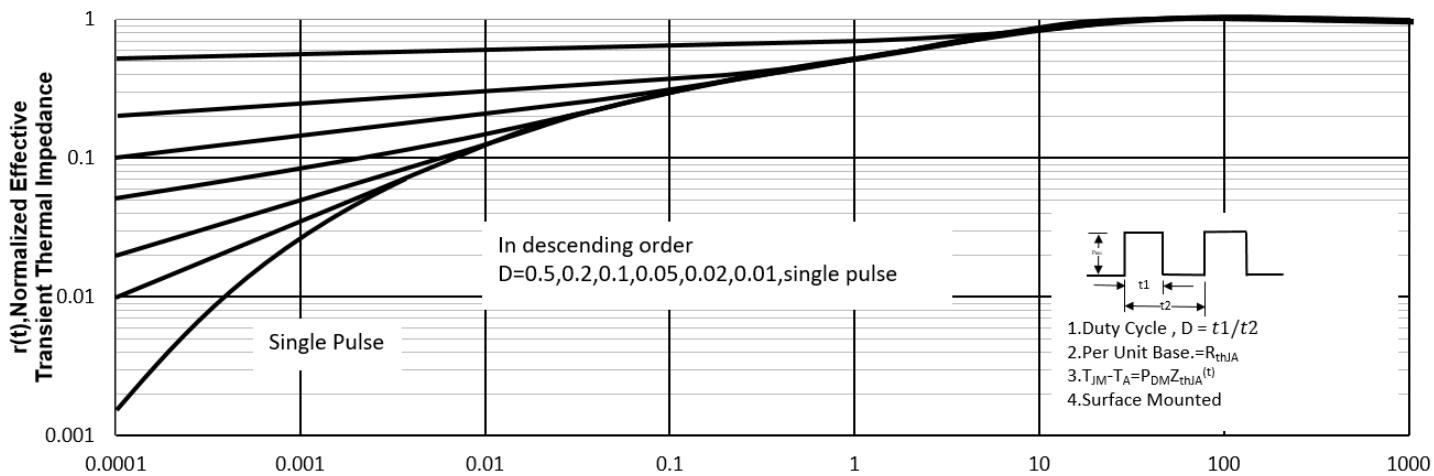
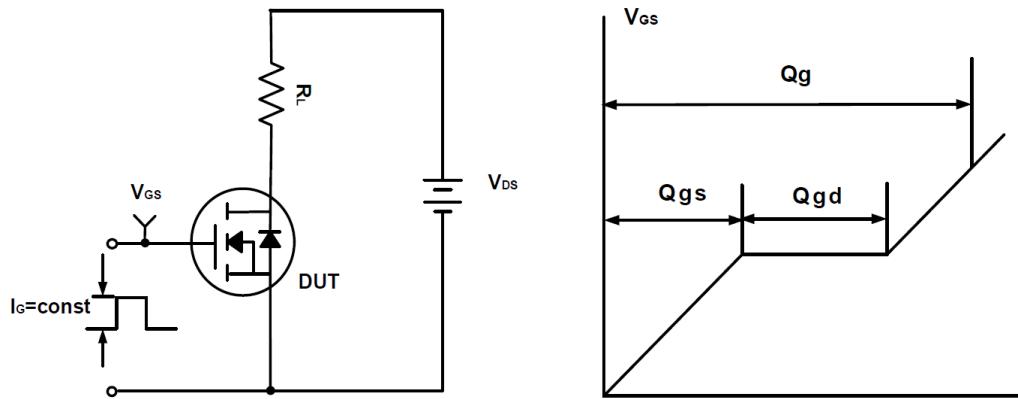
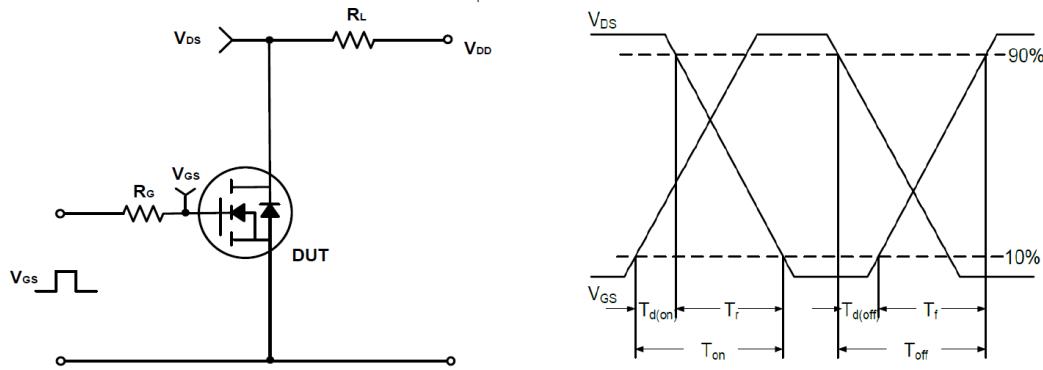
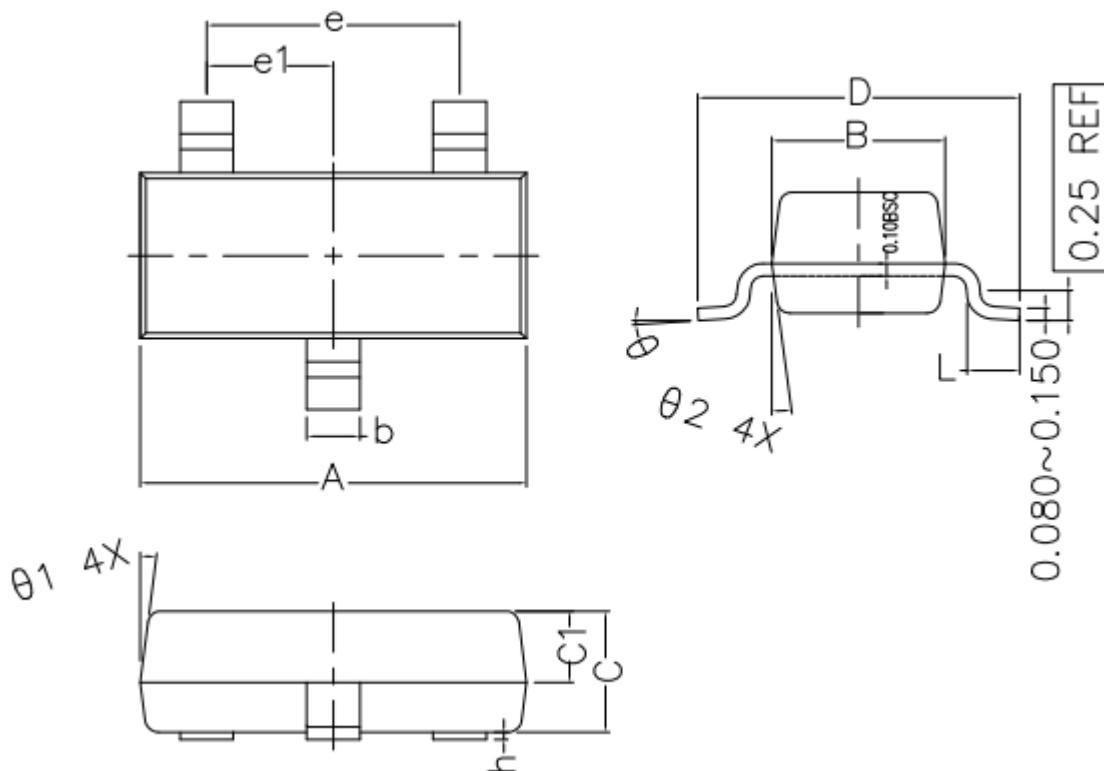


Figure 10: Square Wave Pulse Duration (sec)

**50V/0.34A N-Channel Advanced Power MOSFET**
**Test Circuit and Waveform:**

**Figure A Gate Charge Test Circuit & Waveforms**

**Figure B Switching Test Circuit & Waveforms**

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**SOT-23 Package Outline Dimensions (Units: mm)**



COMMON DIMENSIONS (UNITS OF MEASURE IS mm)			
	MIN	NORMAL	MAX
A	2.800	2.900	3.000
B	1.200	1.300	1.400
C	0.900	1.000	1.100
C1	0.500	0.550	0.600
D	2.250	2.400	2.550
L	0.300	0.400	0.500
h	0.010	0.050	0.100
b	0.300	0.400	0.500
e	1.90 TYPE		
e1	0.95 TYPE		
θ <sub>1</sub>	7° TYPE		
θ <sub>2</sub>	7° TYPE		
θ	0° ~ 7°		